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Disclosure No. 98-1190.02

PTO/SB/21 (05-03)  
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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/646,608	
	Filing Date	August 21, 2003	
	First Named Inventor	Charles E. Larson	
	Art Unit	2812	
	Examiner Name	(To Be Assigned)	
Total Number of Pages in This Submission	4	Attorney Docket Number	108298502US2

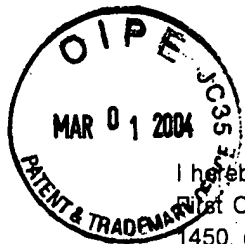
ENCLOSURES (Check all that apply)		
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<div>Remarks</div>		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	David T. Dutcher Registration No. 51,638
Signature	
Date	Feb. 25, 2004

CERTIFICATE OF TRANSMISSION/MAILING			
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Typed or printed name	Stephen P. Whelan		
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Date: February 25, 2004

By: Stephen P. Whelan  
Stephen P. Whelan

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: CHARLES E. LARSON *ET AL.*  
APPLICATION No.: 10/646,608  
FILED: AUGUST 21, 2003  
FOR: **THIN MICROELECTRONIC SUBSTRATES  
AND METHODS OF MANUFACTURE**

EXAMINER: (TO BE ASSIGNED)  
ART UNIT: 2812  
CONF. No: 9809

**Information Disclosure Statement Within Three Months of  
Application Filing or Before First Action – 37 C.F.R. § 1.97(b)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

1. Timing of Submission

This information disclosure is being filed within three months of the filing date of this application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever occurs last [37 C.F.R. § 1.97(b)]. The references listed on the enclosed Form PTO-1449 (modified) may be material to the examination of this application; the Examiner is requested to make them of record in the application.

2. Cited Information

☒ Copies of the following references can be found in parent U.S. Application No. 09/846,057:

- ☒ All cited references  
☐ The following:

3. Effect of Information Disclosure Statement (37 C.F.R. § 1.97(h))

This Information Disclosure Statement is not to be construed as a representation that: (i) a search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the cited information is, or is

considered to be, material to patentability. In addition, applicant does not admit that any enclosed item of information constitutes prior art to the subject invention and specifically reserves the right to demonstrate that any such reference is not prior art.

4. Fee Payment

No fees are believed due because this Information Disclosure Statement is being filed before the mailing date of the first Office Action.

However, should the Commissioner determine that fees are due in order for this Information Disclosure Statement to be considered, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 50-0665.

Respectfully submitted,  
Perkins Coie LLP

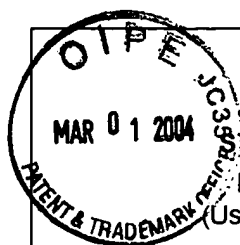


David T. Dutcher  
Registration No. 51,638

Date: Feb. 25, 2004

**Correspondence Address:**

Customer No. 25096  
Perkins Coie LLP  
P.O. Box 1247  
Seattle, Washington 98111-1247  
(206) 359-8000



# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

Form PTO-1449 (Modified)  
(Use several sheets if necessary)

## **COMPLETE IF KNOWN**

<p>Application Number 10/646,608</p> <p>Confirmation Number 9809</p> <p>Filing Date August 21, 2003</p> <p>First Named Inventor Charles E. Larson</p> <p>Group Art Unit 2812</p> <p>Examiner Name (To Be Assigned)</p> <p>Attorney Docket No. 108298502US2</p>				Application Number		10/646,608
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				Group Art Unit		2812
				Examiner Name		(To Be Assigned)
Sheet	1	of	1			

## **U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No.	U.S. Patent or Application		Name of Patentee or Inventor of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			
		5,107,328		Kinsman	04/21/1992	
		5,138,434		Wood et al.	08/11/1992	
		5,374,564		Bruel	12/20/1994	
		5,494,835		Bruel	02/27/1996	
		5,496,755		Bayraktaroglu	03/05/1996	
		5,696,002		Frank et al.	12/09/1997	
		5,773,352		Hamajima	06/30/1998	
		5,938,956		Hembree et al.	08/17/1999	
		5,946,584		Ishikiriya	08/31/1999	
		6,066,514		King et al.	05/23/2000	
		6,089,920		Farnworth et al.	07/18/2000	
		6,096,574		Smith	08/01/2000	
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		6,110,825		Mastromatteo et al.	08/29/2000	
		6,114,221		Tonti et al.	09/05/2000	
		6,225,651	B1	Billon	05/01/2001	
		6,259,153	B1	Corisis	07/10/2001	
		6,294,837	B1	Akram et al.	09/25/2001	
		6,300,670	B1	Kramer et al.	10/09/2001	
		6,303,469	B1	Larson et al.	10/16/2001	
		6,319,751	B1	Lin	11/20/2001	
		6,693,342	B2	Larson et al.	02/17/2004	

## **OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published.	T
		Wolf and Tauber, Silicon Processing for the VLSI Era, Volume 1: Process Technology, pp. 27 and 30-31, 1986, Lattice Press, Sunset Beach, California.	

EXAMINER	DATE CONSIDERED
<p>*EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application(s).</p>	